

The Abstract is amended as follows:

A semiconductor switch circuit is composed of first, first, second, and third semiconductor switches are connected in series together and inserted between an input terminal and an output terminal between input and output terminals and first and second voltage application means are connected in parallel to the first semiconductor switch connected at one end thereof to the input terminal; and to the third semiconductor switch connected at one end thereof to the output terminal, respectively, switches, whereby providing semiconductor switch circuit. Each voltage application means is composed of a series-connected circuit comprising of first or second voltage application semiconductor switch and direct current amplifier which is set to having gain state of approximately +1 and whose input side is connected to the input terminal or the output terminal and a voltage application semiconductor switch connected to said direct current amplifier or output terminal. One end of the respectivefirst or second voltage application semiconductor switch is connected to the junction J of the first and second semiconductor switches, or to the junction K of the second and third semiconductor switches, respectively. The When first through third semiconductor switches are controlled to the OFF and ON state and the turned OFF, first and second voltage application semiconductor switches of the first and second voltage application means to the ON and OFF states in a reverse mode by the switch control means, and when the first through third semiconductor switches are controlled to the OFF state, the first and second voltage application means are turned ON to apply the potential of the input terminal and the output terminal to the junctions J and K, input and output terminals to the junctions J and K.

ABSTRACT

First, second, and third semiconductor switches are connected in series between input and output terminals and first and second voltage application means are connected in parallel to the first and third semiconductor switches, whereby providing semiconductor switch circuit. Each voltage application means is composed of a series-connected circuit of first or second voltage application semiconductor switch and direct current amplifier having gain state of approximately +1 and whose input side is connected to the input or output terminal. One end of the first or second voltage application semiconductor switch is connected to the junction J of the first and second semiconductor switches, or to the junction K of the second and third semiconductor switches, respectively. When first through third semiconductor switches are turned OFF, first and second voltage application semiconductor switches are turned ON to apply the potential of input and output terminals to the junctions J and K.